

Method of Testing Computer System Components

Abstract of the Disclosure

5

An improved method of testing a computer system comprises a sequence of tests that are performed when the computer system is first turned on, according to one embodiment.

The operation of various components of the computer system, such as a chipset, a random access memory, a cache memory, a video controller, a keyboard controller, peripheral

10 memory controllers, busses, and the like, is tested by a test program stored in read only memory (ROM). If the components successfully pass, an address is permanently changed in ROM, so that the test sequence is bypassed whenever the computer system is subsequently booted, saving significant time during future reboots. An improved computer system and a machine-accessible medium are also described.

"Express Mail" mailing label number: EV019077222US

Date of Deposit: February 11, 2002

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.